



COURSE DESCRIPTION CARD - SYLLABUS

Course name

Synchronous hierarchy of digital systems [S2EiT1E-TIT>SHSC]

Course

Field of study

Electronics and Telecommunications

Year/Semester

2/3

Area of study (specialization)

Information and Communication Technologies

Profile of study

general academic

Level of study

second-cycle

Course offered in

English

Form of study

full-time

Requirements

elective

Number of hours

Lecture

30

Laboratory classes

15

Other

0

Tutorials

0

Projects/seminars

0

Number of credit points

4,00

Coordinators

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Lecturers

Prerequisites

none

Course objective

none

Course-related learning outcomes

none

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

none

Programme content

none

Course topics

none

Teaching methods

none

Bibliography

none

Breakdown of average student's workload

	Hours	ECTS
Total workload	0	0,00
Classes requiring direct contact with the teacher	0	0,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	0	0,00